In the most recent Office Action, the examiner has allowed claims 2-4, 8, 9, 11, 12, and 15. The examiner indicated, in that Office Action and in a subsequent telephone interview, that Claims 10 and 13 would be allowable if the reference to claim 1 was deleted from each of those Claims. The reference to claim 1 has been deleted from Claim 10 and from Claim 13 by this Reply.

Claims 1, 5 - 7, and 14, which were rejected in the most recent Office Action, have been withdrawn by this Reply.

Thus, it appears that all remaining claims are in condition for allowance.

In view of the above, it is submitted that the remaining claims are in condition for allowance. Allowance of claims 2-4, 8, 9, 10-13, and 15 at an early date is respectfully solicited.

Respectfully submitted

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## 1. (Withdrawn)

- 2. (Previously Amended) A method of generating a plurality of pseudo-random numbers, said method comprising the steps of:
  - a. Establish initialization values for output series of pseudo-random number matrices  $X_1 X_k$ ;
  - b. Store said initialized pseudo-random number matrices  $X_1 X_k$  in number matrices storage register;
  - c. Establish initialization values for variable transition matrices  $A_{1,1} A_{k,1}$ ;
  - d. Store said initialized transition matrices  $A_{1,1} A_{k,1}$  in transition matrices storage register;
  - e. Establish initialization values for variable augmentation matrices  $B_{1,1} B_{j,1}$ ;
  - f. Store said initialized augmentation matrices  $B_{1,1} B_{j,1}$  in augmentation matrices storage register;
  - g. Establish first modulus operators  $m_{1,1} m_{i,1}$ ;
  - h. Retrieve said transition matrices  $A_{1,1} A_{k,1}$  and apply to said output series of pseudorandom number matrices  $X_1 X_k$  to generate a first intermediate matrix value  $X_{\text{firsttemp}}$ ;
  - i. Retrieve said augmentation matrices  $B_{1,1} B_{j,1}$  and apply to said first intermediate matrix value  $X_{\text{firsttemp}}$  to generate a second intermediate matrix value  $X_{\text{temp}}$ ;
  - j. Sequentially apply said first modulus operators  $m_{1,1} m_{i,1}$  to said second intermediate matrix value  $X_{\text{temp}}$  to generate a first output value of pseudo-random number matrix  $X_n$ ;
  - k. Store said first output value matrix  $X_n$  in said number matrices storage register to establish an updated output series of pseudo-random number matrices  $X_{n-k+1} X_n$ ;
  - 1. Retrieve and extract at least one pseudo-random number from element entries of said number matrices storage register;
  - m. Provide each pseudo-random number to long-term storage register for use in device which can employ pseudo-random numbers;
  - n. Retrieve and update said transition matrices  $A_{1,1} A_{k,1}$  through updating process to create and store updated transition matrices  $A_{1,2} A_{k,2}$ ;
  - o. Retrieve said updated transition matrices  $A_{1,2} A_{k,2}$  and apply to said updated output series of pseudo-random number matrices  $X_{n-k+1} X_n$  to generate an updated first intermediate matrix value  $X_{\text{firsttemp}}$ ;
  - p. Retrieve and update said augmentation matrices  $B_{1,1} B_{j,1}$  through updating process to create and store updated augmentation matrices  $B_{1,2} B_{j,2}$ ;
  - q. Retrieve said updated augmentation matrices  $B_{1,2} B_{j,2}$  and apply to said updated first intermediate matrix value  $X_{\text{firsttemp}}$  to generate an updated second intermediate matrix value  $X_{\text{temp}}$ ;
  - r. Update said first modulus operators  $m_{1,1} m_{i,1}$  through updating process to create updated first modulus operators  $m_{1,2} m_{i,2}$ ;
  - s. Sequentially apply said updated first modulus operators  $m_{1,2}-m_{i,2}$  to said updated second intermediate matrix value  $X_{temp}$  to generate a second output value of pseudo-random number matrix  $X_{n+1}$  from which at least one pseudo-random number is extracted; and
  - t. Store said second pseudo-random number matrix  $X_{n+1}$  in said number matrices storage register.
- 3. (Previously Amended) A method of generating a plurality of pseudo-random numbers according to claim 2, wherein said steps l. through t. are repeated to generate a desired number d of pseudo-random number matrices X<sub>n+d</sub> from which a plurality of element entries of said pseudo-random number matrices are extracted as pseudo-random numbers and provided to long-term storage register for use in device which can employ pseudo-random numbers.
- 4. (Original) A method according to claim 2 further comprising the step of:



Selecting a first subset of said pseudo-random numbers from said updated output series of pseudo-random number matrices.

- 5. (Withdrawn)
- 6. (Withdrawn)
- 7. (Withdrawn)
- 8. (Original) A method according to claim 2, further comprising the steps of:
  - a. Establish second modulus operators  $r_{1,1} r_{g,1}$ ;
  - b. Sequentially apply and update second modulus operators  $r_{1,1} r_{g,1}$ ,  $r_{1,2} r_{g,2}$ , ...  $r_{1,n+d-k} r_{g,n+d-k}$  to said updated output series of pseudo-random number matrices to generate a second output series of pseudo-random number matrices.
- 9. (Previously Amended) A method according to claim 8, further comprising the step of:
  Selecting a second subset of said pseudo-random numbers from element entries of said second output series of pseudo-random number matrices.
- 10. (Currently Amended) A method according to claim 2 or claim 3:
  - a. Wherein said first modulus operators  $m_{1,1} m_{j,1}$ ,  $m_{1,2} m_{j,2}$ , ...  $m_{1,n+d-k} m_{j,n+d-k}$  comprise a uniform variable modular reduction, and
  - b. Further comprising the step of discarding certain pseudo-random numbers which are not uniformly distributed.
- 11. (Original) A method according to claim 8:
  - a. Wherein said second modulus operators  $r_{1,1} r_{g,1}$ ,  $r_{1,2} r_{g,2}$ , ...  $r_{1,n+d-k} r_{g,n+d-k}$  comprise a uniform variable modular reduction, and
  - b. Further comprising the step of discarding certain pseudo-random numbers which are not uniformly distributed.
- 12. (Previously Amended) A method according to claim 2 or claim 3, further comprising the steps of:
  - a. Create at least one alternate storage register of pseudo-random number matrices by separately taking steps a t;
  - b. Create temporary composite pseudo-random number matrices by combining each resulting storage register of pseudo-random number matrices through at least one mathematical operation;
  - c. Create final composite pseudo-random number matrices by applying variable modular reduction to said temporary composite pseudo-random number matrices; and
  - d. Select a subset of pseudo-random numbers from element entries of said resulting final composite pseudo-random number matrices.
- 13. (Twice Amended) A method according to claim 2 or claim 3 further comprising:
  - a. Apply an invertibility evaluation module to each second intermediate matrix value X<sub>temp</sub>;
  - b. Adjust augmentation matrices  $B_{1,1} B_{j,1}$ ,  $B_{1,2} B_{j,2}$ , ...  $B_{1,n+d-1} B_{j,n+d-1}$ , so that said second intermediate matrix value  $X_{temp}$  is non-invertible;
  - c. Sequentially apply said first modulus operators  $m_{1,1} m_{i,1}$  to said non-invertible second intermediate matrix value  $X_{temp}$  to generate output value of non-invertible pseudo-random number matrix  $X_n$  from which at least one pseudo-random number is extracted; and

d. Select a subset of pseudo-random number output values from element entries of said non-invertible pseudo-random number matrices.

## 14. (Withdrawn)

- 15. (Previously Amended) An apparatus for generating a plurality of pseudo-random numbers, said apparatus comprising:
  - a. Output matrices initialization means for establishing and storing initialization values for output series of pseudo-random number matrices  $X_1 X_k$  by assigning values to matrix entries;
  - b. Transition matrices initialization means for establishing and storing initialization values for variable transition matrices  $A_{1,1} A_{k,1}$  by assigning values to matrix entries;
  - c. Augmentation matrices initialization means for establishing and storing initialization values for variable augmentation matrices  $B_{1,1} B_{j,1}$  by assigning values to matrix entries;
  - d. Modulus operator initialization means for establishing first modulus operators  $m_{1,1} m_{i,1}$  by assigning values to modulus operators;
  - e. First application means for retrieving and applying said transition matrices  $A_{1,1} A_{k,1}$  to said output series of pseudo-random number matrices  $X_1 X_k$  to generate a first intermediate matrix value  $X_{\text{firsttemp}}$ ;
  - f. Second application means for retrieving and applying said augmentation matrices  $B_{1,1}$   $B_{j,1}$  to said first intermediate matrix value  $X_{firsttemp}$  to generate a second intermediate matrix value  $X_{temp}$ ;
  - g. Third application means for sequentially applying said first modulus operators  $m_{1,1} m_{i,1}$  to said second intermediate matrix value  $X_{temp}$  to generate and store a first output value of pseudo-random number matrix  $X_n$  from element entries of which at least one pseudo-random number is extracted and provided to long-term storage for use in device which can employ pseudo-random numbers;
  - h. Storage means for storing said first output value matrix  $X_n$  in a storage register to establish an updated output series of pseudo-random number matrices;
  - i. Transition matrices updating means for retrieving and updating said transition matrices  $A_{1,1} A_{k,1}$  to create and store updated transition matrices  $A_{1,2} A_{k,2}$ ;
  - j. Fourth application means for retrieving and applying said updated transition matrices  $A_{1,2}$   $A_{k,2}$  to said updated output series of pseudo-random number matrices  $X_{n-k+1} X_n$  to generate an updated first intermediate matrix value  $X_{firsttemp}$ ;
  - k. Augmentation matrices updating means for retrieving and updating said augmentation matrices  $B_{1,1} B_{j,1}$  to create and store updated augmentation matrices  $B_{1,2} B_{j,2}$ ;
  - 1. Fifth application means for retrieving and applying said updated augmentation matrices  $B_{1,2} B_{j,2}$  to said updated first intermediate matrix value  $X_{\text{firsttemp}}$  to generate an updated second intermediate matrix value  $X_{\text{temp}}$ ;
  - m. Modulus operator updating means for updating said first modulus operators  $m_{1,1} m_{i,1}$  to create updated first modulus operators  $m_{1,2} m_{i,2}$ ;
  - n. Sixth application means for sequentially applying said updated first modulus operators  $m_{1,2} m_{i,2}$  to said updated second intermediate matrix value  $X_{temp}$  to generate a second output value of pseudo-random number matrix  $X_{n+1}$  from element entries of which at least one pseudo-random number is extracted and provided to long-term storage register for use in device which can employ pseudo-random numbers; and
  - o. Second storage means for storing said second pseudo-random number matrix  $X_{n+1}$  in said number matrices storage register.

1.(Withdrawn) A method of generating a pseudo-random number, said method comprising the steps of:

a. Establish initialization values for output series of pseudo-random number matrices  $X_1 - X_k$ ;

b. Store said initialized pseudo-random number matrices  $X_1 - X_k$  in number matrices storage register:

e. Establish initialization values for variable transition matrices  $A_{1,1} - A_{k,1}$ ;

d.Store said initialized transition matrices  $A_{1,1}$ — $A_{k,1}$  in transition matrices storage register;

e. Establish initialization values for variable augmentation matrices B<sub>1,1</sub>—B<sub>1,1</sub>;

f.Store said initialized augmentation matrices B<sub>1,1</sub>—B<sub>j,1</sub>in augmentation matrices storage register;

g. Establish first modulus operators m<sub>1,1</sub> - m<sub>i,1</sub>;

h.Retrieve said transition matrices  $A_{1,1}$ — $A_{k,1}$  and apply to said output series of pseudo random number matrices  $X_1$ — $X_k$  to generate a first intermediate matrix value  $X_{firstlemp}$ ;

i.Retrieve said augmentation matrices  $B_{1,1}$ — $B_{j,1}$  and apply to said first intermediate matrix value  $X_{temp}$ ;

j. Sequentially apply said first modulus operators  $m_{1,1} - m_{i,1}$  to said second intermediate matrix value  $X_{temp}$  to generate an output value of pseudo-random number matrix  $X_n$ ;

k. Store said first output value matrix X<sub>n</sub> in number matrices storage register;

L.Retrieve and extract at least one pseudo-random number from element entries of said number matrices storage register; and

m.a. Provide said pseudo-random number to long term storage register for use in device which can employ pseudo-random numbers.

- 2. (Previously Amended) A method of generating a plurality of pseudo-random numbers, said method comprising the steps of:
  - a. Establish initialization values for output series of pseudo-random number matrices  $X_1 X_k$ ;
  - b. Store said initialized pseudo-random number matrices  $X_1 X_k$  in number matrices storage register;
  - c. Establish initialization values for variable transition matrices  $A_{1,1} A_{k,1}$ ;
  - d. Store said initialized transition matrices  $A_{1,1} A_{k,l}$  in transition matrices storage register;
  - e. Establish initialization values for variable augmentation matrices  $B_{1,1} B_{j,1}$ ;
  - f. Store said initialized augmentation matrices  $B_{1,1} B_{j,1}$  in augmentation matrices storage register;
  - g. Establish first modulus operators  $m_{1,1} m_{i,1}$ ;
  - h. Retrieve said transition matrices  $A_{1,1} A_{k,1}$  and apply to said output series of pseudorandom number matrices  $X_1 X_k$  to generate a first intermediate matrix value  $X_{\text{firsttemp}}$ ;
  - i. Retrieve said augmentation matrices  $B_{1,1} B_{j,1}$  and apply to said first intermediate matrix value  $X_{\text{firsttemp}}$  to generate a second intermediate matrix value  $X_{\text{temp}}$ ;
  - j. Sequentially apply said first modulus operators  $m_{1,1} m_{i,1}$  to said second intermediate matrix value  $X_{\text{temp}}$  to generate a first output value of pseudo-random number matrix  $X_n$ ;
  - k. Store said first output value matrix  $X_n$  in said number matrices storage register to establish an updated output series of pseudo-random number matrices  $X_{n-k+1} X_n$ ;
  - 1. Retrieve and extract at least one pseudo-random number from element entries of said number matrices storage register;
  - m. Provide each pseudo-random number to long-term storage register for use in device which can employ pseudo-random numbers;
  - n. Retrieve and update said transition matrices  $A_{1,1} A_{k,1}$  through updating process to create and store updated transition matrices  $A_{1,2} A_{k,2}$ ;
  - o. Retrieve said updated transition matrices  $A_{1,2} A_{k,2}$  and apply to said updated output series of pseudo-random number matrices  $X_{n-k+1} X_n$  to generate an updated first intermediate matrix value  $X_{\text{firsttemp}}$ ;

- p. Retrieve and update said augmentation matrices  $B_{1,1} B_{j,1}$  through updating process to create and store updated augmentation matrices  $B_{1,2} B_{j,2}$ ;
- q. Retrieve said updated augmentation matrices  $B_{1,2} B_{j,2}$  and apply to said updated first intermediate matrix value  $X_{\text{firsttemp}}$  to generate an updated second intermediate matrix value  $X_{\text{temp}}$ ;
- r. Update said first modulus operators  $m_{1,1} m_{i,1}$  through updating process to create updated first modulus operators  $m_{1,2} m_{i,2}$ ;
- s. Sequentially apply said updated first modulus operators  $m_{1,2} m_{i,2}$  to said updated second intermediate matrix value  $X_{temp}$  to generate a second output value of pseudo-random number matrix  $X_{n+1}$  from which at least one pseudo-random number is extracted; and
- t. Store said second pseudo-random number matrix  $X_{n+1}$  in said number matrices storage register.
- 3. (Previously Amended) A method of generating a plurality of pseudo-random numbers according to claim 2, wherein said steps l. through t. are repeated to generate a desired number d of pseudo-random number matrices X<sub>n+d</sub> from which a plurality of element entries of said pseudo-random number matrices are extracted as pseudo-random numbers and provided to long-term storage register for use in device which can employ pseudo-random numbers.
- 4. (Original) A method according to claim 2 further comprising the step of:
  Selecting a first subset of said pseudo-random numbers from said updated output series of pseudo-random number matrices.
- 5. (Withdrawn) A method according to claim 1, claim 2, or claim 3, wherein k = 1 so that a single variable transition matrix is used.
- 6. (Withdrawn) A method according to claim 1, claim 2, or claim 3, where j = 1 so that a single variable augmentation matrix is used.
- 7. (Withdrawn) A method according to claim 1, claim 2, or claim 3, where i = 1 so that a single modulus operator is used.
- 8. (Original) A method according to claim 2, further comprising the steps of:
  - a. Establish second modulus operators  $r_{1,1} r_{g,1}$ ;
  - b. Sequentially apply and update second modulus operators  $r_{1,1} r_{g,1}$ ,  $r_{1,2} r_{g,2}$ , ...  $r_{1,n+d-k} r_{g,n+d-k}$  to said updated output series of pseudo-random number matrices to generate a second output series of pseudo-random number matrices.
- 9. (Previously Amended) A method according to claim 8, further comprising the step of:
  Selecting a second subset of said pseudo-random numbers from element entries of said second output series of pseudo-random number matrices.
- 10. (Currently Amended) A method according to claim 1, claim 2, or claim 3:
  - a. Wherein said first modulus operators  $m_{1,1} m_{j,1}$ ,  $m_{1,2} m_{j,2}$ , ...  $m_{1,n+d-k} m_{j,n+d-k}$  comprise a uniform variable modular reduction, and
  - b. Further comprising the step of discarding certain pseudo-random numbers which are not uniformly distributed.
- 11. (Original) A method according to claim 8:
  - a. Wherein said second modulus operators  $r_{1,1} r_{g,1}$ ,  $r_{1,2} r_{g,2}$ , ...  $r_{1,n+d-k} r_{g,n+d-k}$  comprise a uniform variable modular reduction, and

- b. Further comprising the step of discarding certain pseudo-random numbers which are not uniformly distributed.
- 12. (Previously Amended) A method according to claim 2 or claim 3, further comprising the steps of:

. . .

- a. Create at least one alternate storage register of pseudo-random number matrices by separately taking steps a t;
- b. Create temporary composite pseudo-random number matrices by combining each resulting storage register of pseudo-random number matrices through at least one mathematical operation;
- c. Create final composite pseudo-random number matrices by applying variable modular reduction to said temporary composite pseudo-random number matrices; and
- d. Select a subset of pseudo-random numbers from element entries of said resulting final composite pseudo-random number matrices.
- 13. (Twice Amended) A method according to elaim 1, claim 2, or claim 3 further comprising:
  - a. Apply an invertibility evaluation module to each second intermediate matrix value  $X_{temp}$ ;
  - b. Adjust augmentation matrices  $B_{1,1} B_{j,1}$ ,  $B_{1,2} B_{j,2}$ , ...  $B_{1,n+d-1} B_{j,n+d-1}$ , so that said second intermediate matrix value  $X_{temp}$  is non-invertible;
  - c. Sequentially apply said first modulus operators  $m_{1,1} m_{i,1}$  to said non-invertible second intermediate matrix value  $X_{temp}$  to generate output value of non-invertible pseudo-random number matrix  $X_n$  from which at least one pseudo-random number is extracted; and
  - d. Select a subset of pseudo-random number output values from element entries of said non-invertible pseudo-random number matrices.
- 14. (Withdrawn) An apparatus for generating a pseudo-random number, said apparatus comprising:
  a.Output matrices initialization means for establishing and storing initialization values for output series of pseudo-random number matrices X<sub>1</sub>—X<sub>k</sub> by assigning values to matrix entries;
  - b. Transition matrices initialization means for establishing and storing initialization values for variable transition matrices  $A_{l,l}$ — $A_{k,l}$  by assigning values to matrix entries;
  - e.Augmentation matrices initialization means for establishing and storing initialization values for variable augmentation matrices  $B_{I,I} B_{j,I}$  by assigning values to matrix entries;
  - d.Modulus operator initialization means for establishing first modulus operators m<sub>1,1</sub> by assigning values to modulus operators;
  - e.First application means for retrieving and applying said transition matrices  $A_{L,l} A_{k,L}$  to said output series of pseudo-random number matrices  $X_l X_k$  to generate a first intermediate matrix value  $X_{\text{firstlemp}}$ ;
  - f.Second application means for retrieving and applying said augmentation matrices  $B_{I,I} B_{j,I}$  to said-first intermediate matrix value  $X_{\text{firstlemp}}$  to generate a second intermediate matrix value  $X_{\text{temp}}$ ; and
  - g.a. Third application means for sequentially applying said first modulus operators m<sub>1,1</sub> m<sub>i,1</sub> to said second intermediate matrix value X<sub>temp</sub> to generate and store an output value of pseudo-random number matrix X<sub>n</sub> from element entries of which at least one pseudo-random number is extracted and provided to long-term storage for use in device which can employ pseudo-random numbers.
- 15. (Previously Amended) An apparatus for generating a plurality of pseudo-random numbers, said apparatus comprising:
  - a. Output matrices initialization means for establishing and storing initialization values for output series of pseudo-random number matrices  $X_1 X_k$  by assigning values to matrix entries;

- b. Transition matrices initialization means for establishing and storing initialization values for variable transition matrices  $A_{1,1} A_{k,1}$  by assigning values to matrix entries;
- c. Augmentation matrices initialization means for establishing and storing initialization values for variable augmentation matrices  $B_{1,1} B_{j,1}$  by assigning values to matrix entries;
- d. Modulus operator initialization means for establishing first modulus operators  $m_{1,1} m_{i,1}$  by assigning values to modulus operators;
- e. First application means for retrieving and applying said transition matrices  $A_{1,1} A_{k,1}$  to said output series of pseudo-random number matrices  $X_1 X_k$  to generate a first intermediate matrix value  $X_{\text{firsttemp}}$ ;
- f. Second application means for retrieving and applying said augmentation matrices  $B_{i,1}$   $B_{j,1}$  to said first intermediate matrix value  $X_{\text{firsttemp}}$  to generate a second intermediate matrix value  $X_{\text{temp}}$ ;
- g. Third application means for sequentially applying said first modulus operators  $m_{1,1} m_{i,1}$  to said second intermediate matrix value  $X_{temp}$  to generate and store a first output value of pseudo-random number matrix  $X_n$  from element entries of which at least one pseudo-random number is extracted and provided to long-term storage for use in device which can employ pseudo-random numbers;
- h. Storage means for storing said first output value matrix  $X_n$  in a storage register to establish an updated output series of pseudo-random number matrices;
- i. Transition matrices updating means for retrieving and updating said transition matrices  $A_{1,1} A_{k,1}$  to create and store updated transition matrices  $A_{1,2} A_{k,2}$ ;
- j. Fourth application means for retrieving and applying said updated transition matrices  $A_{1,2}$   $A_{k,2}$  to said updated output series of pseudo-random number matrices  $X_{n-k+1} X_n$  to generate an updated first intermediate matrix value  $X_{firsttemp}$ ;
- k. Augmentation matrices updating means for retrieving and updating said augmentation matrices  $B_{1,1} B_{j,1}$  to create and store updated augmentation matrices  $B_{1,2} B_{j,2}$ ;
- 1. Fifth application means for retrieving and applying said updated augmentation matrices  $B_{1,2} B_{j,2}$  to said updated first intermediate matrix value  $X_{firsttemp}$  to generate an updated second intermediate matrix value  $X_{temp}$ ;
- m. Modulus operator updating means for updating said first modulus operators  $m_{1,1} m_{i,1}$  to create updated first modulus operators  $m_{1,2} m_{i,2}$ ;
- n. Sixth application means for sequentially applying said updated first modulus operators  $m_{1,2}-m_{i,2}$  to said updated second intermediate matrix value  $X_{temp}$  to generate a second output value of pseudo-random number matrix  $X_{n+1}$  from element entries of which at least one pseudo-random number is extracted and provided to long-term storage register for use in device which can employ pseudo-random numbers; and
- o. Second storage means for storing said second pseudo-random number matrix  $X_{n+1}$  in said number matrices storage register.